

DATE: <u>6.18.02</u>	FROM: <u>UE</u> (print name)
FORWARD TO:	REASON(S):
A. Art Unit: <u>2185</u>	A. You had Parent <input type="checkbox"/> (check box)
B. Class: <u>713</u>	B. See Title <input type="checkbox"/> (check box)
C Subclass: <u>500+</u>	C. See Abstract <input type="checkbox"/> (check box)
	D. See Claim(s): _____

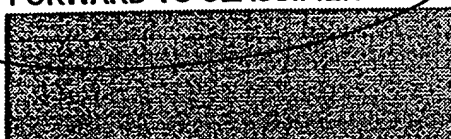
FURTHER EXPLANATION IF NEEDED:

Clock distribution system.

DATE: <u>10/8/02</u>	FROM: <u>CSO</u> (print name)
FORWARD TO:	REASON(S):
A. Art Unit: <u>2812</u>	A. You had Parent <input type="checkbox"/> (check box)
B. Class: <u>438</u>	B. See Title <input type="checkbox"/> (check box)
C Subclass: _____	C. See Abstract <input type="checkbox"/> (check box)
	D. See Claim(s): _____

FURTHER EXPLANATION IF NEEDED:

semiconductor device circuitry

DATE: <u>10/21/02</u>	FROM: <u>J. Wiebling</u> (print name)
FORWARD TO CLASSIFIER	REASON(S):
	A. You had Parent <input type="checkbox"/> (check box)
	B. See Title <input type="checkbox"/> (check box)
	C. See Abstract <input type="checkbox"/> (check box)
	D. See Claim(s): _____

FURTHER EXPLANATION IF NEEDED:

No methods of making device for cl 438

DISPOSITION BY 2700 CLASSIFICATION

DATE: _____	CLASSIFIER: _____
FORWARD TO:	REASON(S):
A. Art Unit: _____	A. You had Parent <input type="checkbox"/> (check box)
B. Class: _____	B. See Title <input type="checkbox"/> (check box)
C Subclass: _____	C. See Abstract <input type="checkbox"/> (check box)
	D. See Claim(s): _____

FURTHER EXPLANATION IF NEEDED:

Not structure of a semiconductor device to be in class 257
 Not SE manufacturing to be in class 438, too much for 327.
 Method of determining & processing for Fractional-N-programmable value.
 systems controlled by clock signal.
 ⇒ proper for class 713/500+ (claims 11+) or class 375/